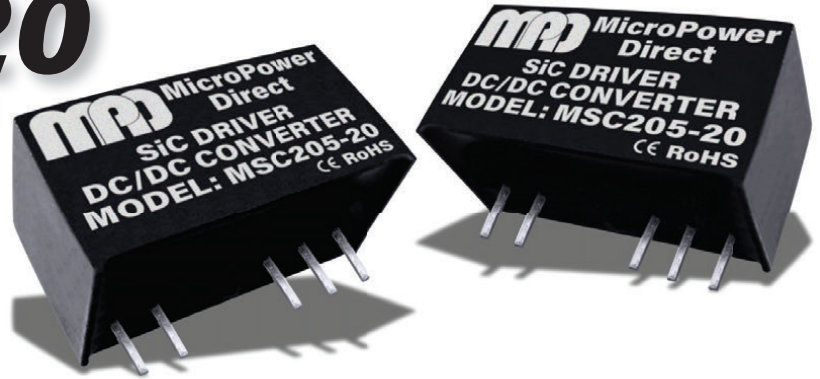


MSC205-20

Compact, 3 kV ISO SiC MOSFET Driver DC/DC Converter



Key Features:

- +5 VDC Input
- +20/-5 VDC Outputs
- 79% Efficiency
- -40°C to 105°C Operation
- 3,000 VAC Isolation
- Low Isolation Capacitance
- Miniature SIP Case
- >3.5 MHour MTBF
- Short Circuit Protection
- Industry Standard Pin-Out

RoHS



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Electrical Specifications

Specifications typical @ +25°C, nominal input voltage & rated output current, unless otherwise noted. Specifications subject to change without notice.

Input

Parameter	Conditions	Min.	Typ.	Max.	Units
Supply Voltage Range		4.50		5.50	VDC
Input Filter	Internal Capacitor				

Output

Parameter	Conditions	Min.	Typ.	Max.	Units
Output Voltage Accuracy	See Tolerance Graphs On Page 2				
Line Regulation, See Note 1			±1.4	±2.0	%
Load Regulation, See Note 2	20 V _{OUT}		8.0	12.0	%
	-5 V _{OUT}		10.0	14.0	%
Output Ripple (20 MHz)	See Note 3		40		mV P - P
Output Noise (20 MHz)	See Note 3		75		
Capacitive Load				100	μF
Efficiency		75	79		%
Temperature Coefficient			±0.03		%/°C
Output Short Circuit	Continuous (Autorecovery)				

General

Parameter	Conditions	Min.	Typ.	Max.	Units
Isolation Voltage	60 Seconds	3,000			VAC
		5,200			VDC
Isolation Resistance	500 VDC	1,000			MΩ
Isolation Capacitance	100 kHz/0.1V		3.5		pF
Switching Frequency			100		kHz

EMI Characteristics

Parameter	Standard	Criteria	Level
Radiated Emissions, See Note 4	EN 55032		Class B
Conducted Emissions, See Note 4	EN 55032		Class B
ESD	EN 61000-4-2	B	±6 kV Contact

Environmental

Parameter	Conditions	Min.	Typ.	Max.	Units
Operating Temperature Range	Ambient	-40	+25	+105	°C
Storage Temperature Range		-55		+125	°C
Cooling	Free Air Convection				
Humidity	RH, Non-condensing			95	%

Physical

Case Size and Weight	See Mechanical Diagram (Page 4)				
Case Material	Non-Conductive Black Plastic (UL94-V0)				

Reliability Specifications

Parameter	Conditions	Min.	Typ.	Max.	Units
MTBF	MIL HDBK 217F, 25°C, Gnd Benign	3.5			MHours

Absolute Maximum Ratings

Parameter	Conditions	Min.	Typ.	Max.	Units
Max Supply Voltage (1 Sec)				9.0	VDC
Lead Temperature	1.5 mm From Case For 10 Sec			300	°C

Caution: Exceeding Absolute Maximum Ratings may damage the module. These are not continuous operating ratings.

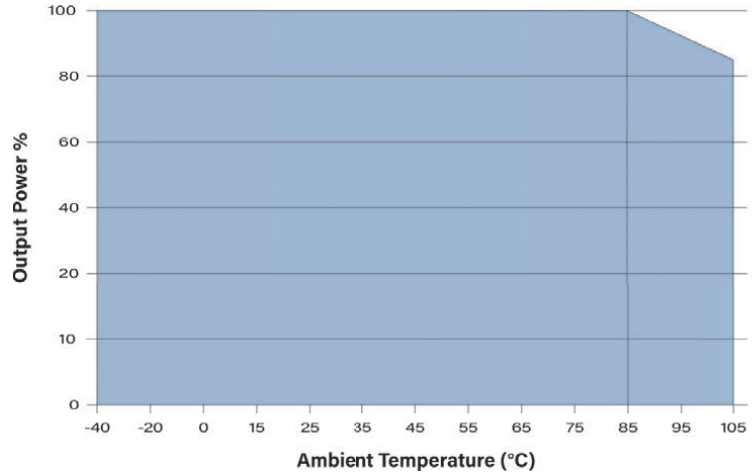
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Model Number	Input (Supply)				Output 1			Output 2			Fuse Rating Slow-Blow (mA)
	Voltage (VDC)		Current (mA)		Voltage	Current (mA)		Voltage	Current (mA)		
	Nom.	Range	Full Load	No Load	Nom. (VDC)	Max	Min	Nom. (VDC)	Max	Min	
MSC205-20	5.0	4.50 - 5.50	456	53	20.0	80.0	8.0	-5.0	-40.0	-4.0	900

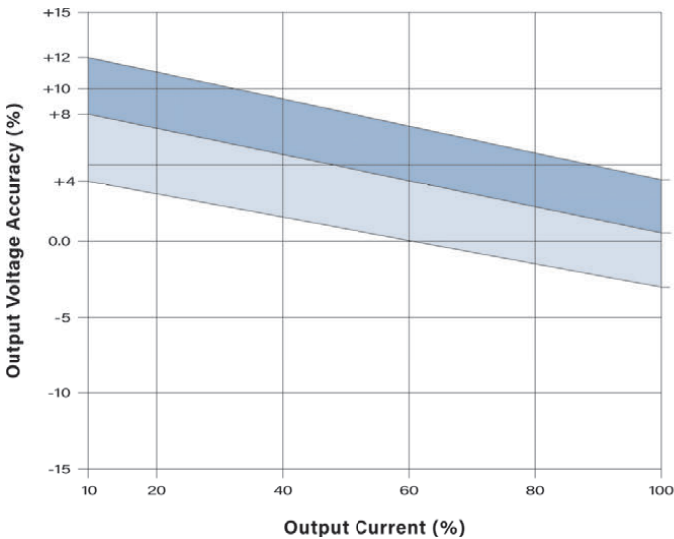
Notes:

1. Line regulation is measured for an input voltage change of $\pm 10\%$.
2. Load regulation is measured from 10% load to full load.
3. When measuring output ripple & noise, it is recommended that an external capacitor ($1 \mu\text{F}$ to $10 \mu\text{F}$) be placed from each output to common.
4. The unit will meet the radiated and conducted EMI specifications with the addition of external components as shown in the connection diagram on page 3. These components are shown inside the dotted line box at the bottom of the illustration
5. Operation at no-load will not damage these units. However, they may not meet all specifications.
6. It is recommended that a fuse be used on the input of a power supply for protection. See the Model Selection table above for the correct rating.

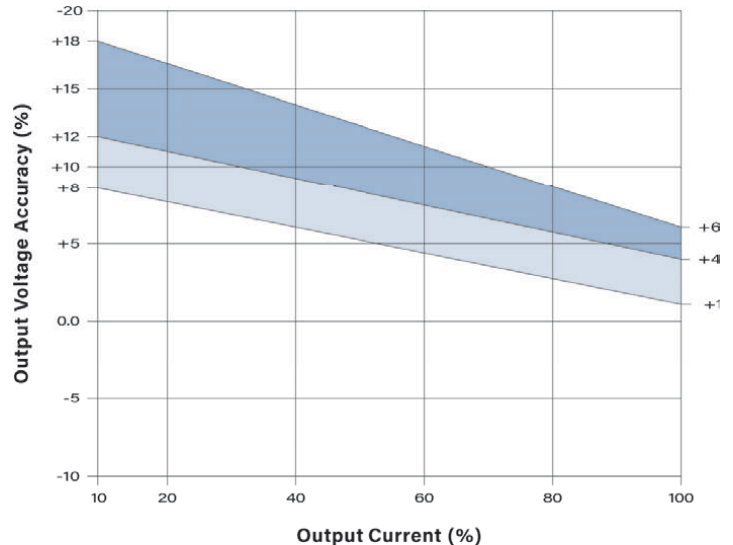
Derating Curve



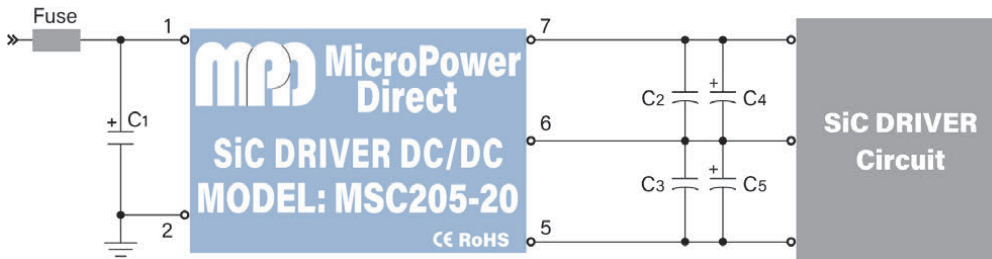
Output Voltage Tolerance: +20V Output



Output Voltage Tolerance: -5V Output



Simple Connection



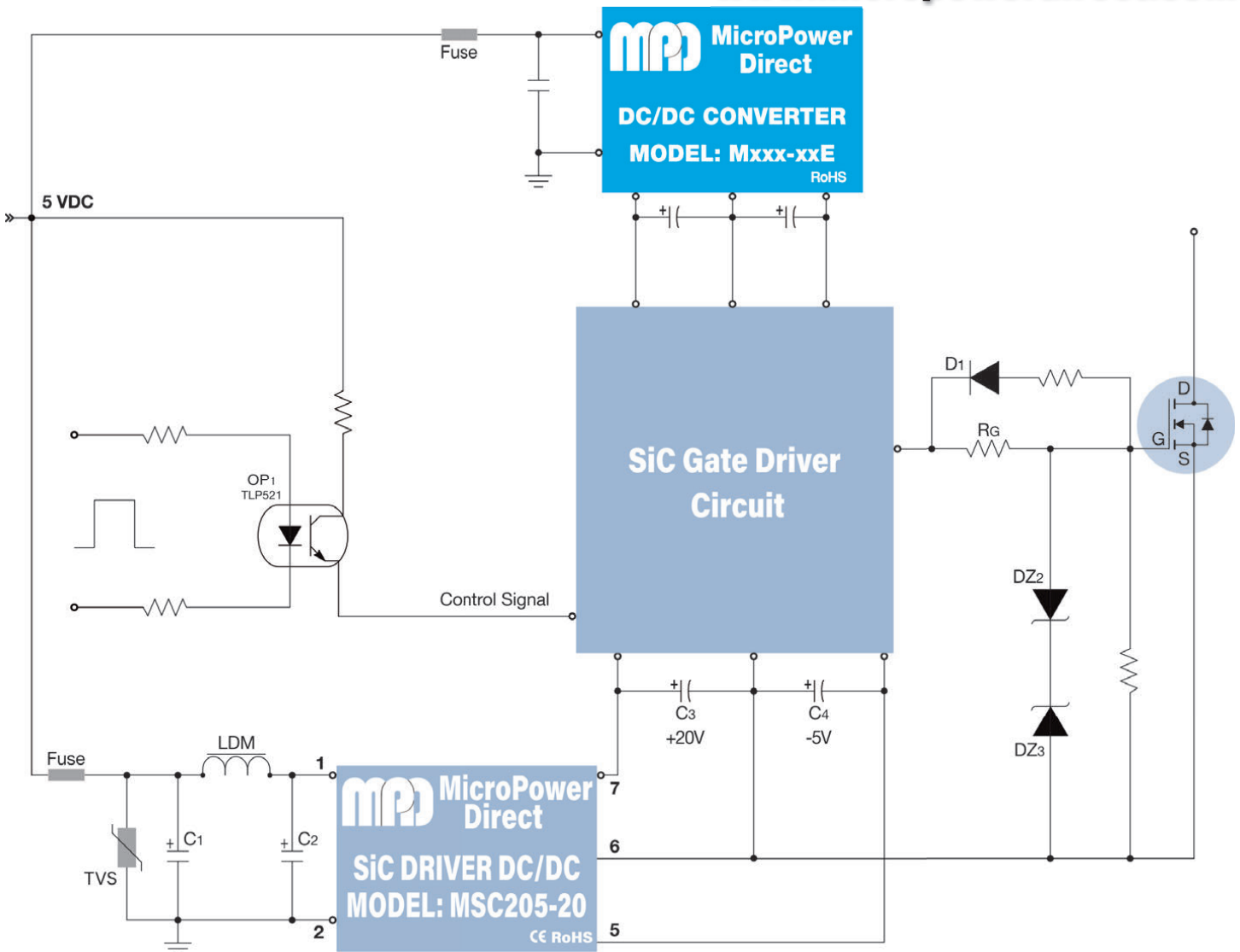
Recommended component values:

Component	Input Voltage
	5 VDC
C1	100 μF /35V
C2, C3	1.0 μF to 10 μF
C4, C5	100 μF /35V

The MSC205-20 is designed to be used in the driver circuits for silicon carbide (SiC) MOSFETs. The asymmetrical outputs of the MSC205-20 provide the positive/negative gate bias needed to efficiently switch the MOSFET.

The circuit above shows a simple connection to a driver circuit. Low ESR electrolytic capacitors should be used. The recommended values for all capacitors is given in the chart at the right of the diagram.

A more complete driver power circuit is illustrated on page 3.



The **MSC205-20** is specifically designed for use in gate driver circuits. With asymmetrical outputs of +20 VDC & -5 VDC, an isolation barrier specified at 3.5 kVAC, very low isolation capacitance and a wide operating temperature range; they are an ideal choice for Silicon Carbide (SiC) MOSFET drive & control circuits.

SiC MOSFETs are often used in high voltage, very high frequency applications. The figure above illustrates a typical connection to a driver circuit. Again, **MPD** offers a number of power products that can be used in gate driver circuits (IGBT and SiC).

The circuit above uses two **MPD** parts. At the top, a DC/DC converter is used convert the 5 VDC system bus into voltage levels required by the driver components (if required). This converter also isolates the driver circuit from the power bus. **MPD** offers hundreds of standard DC/DC converters at a variety of power levels that can be used for this purpose.

The **MSC205-20** converts the input 5 VDC into asymmetrical +20 VDC & -5.0 VDC outputs. These outputs are used to set up the positive/minus gate bias required for high and low side switching.

The **MSC205-20** also provides power isolation for the gate drive. It is specified for I/O isolation

levels of 3.0 kVAC & 5.2 kVDC. The optocoupler provides isolation for the control signal.

Some notes on the **MSC205-20** connection (starting with the input) are:

1. The **MSC205-20** DC/DC should be mounted as close to the SiC driver circuit as possible, to minimize the length of connecting board traces or wires.
2. The **MSC205-20** does not include overload protection (typical of most low power DC/DC's). It is recommended that an external fuse be used. The recommended fuse is shown in the model chart on page two.
3. The **MSC205-20** series does not include internal protection against voltage transients that might occur on the power line (again, this is typical of most low power DC/DC's). It is recommended that an external suppressor be used. We have included a TVS placed in front of the input filtering components. The recommended value is shown in the table at right.
4. The addition of the input filter components (C1, C2 and LDM) will typically bring the **MSC205-20** circuit to within the limits of EN 55032 Class B. The recommended values for these components are shown in the table at right.

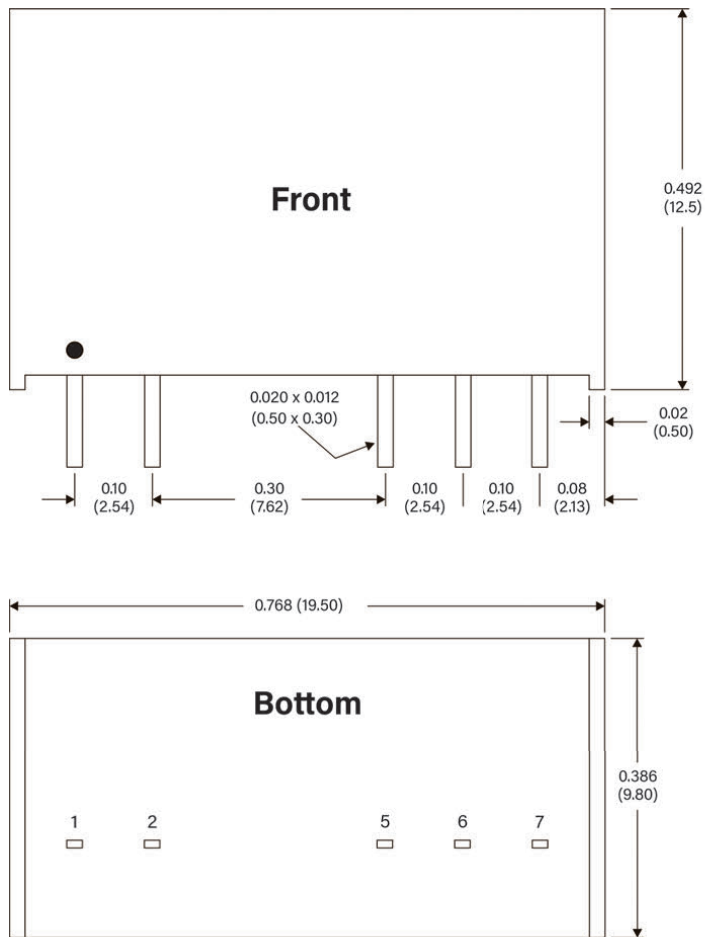
If meeting EN 55032 class A or B is not a concern, the inductor (LDM) and one capacitor (C2) can be eliminated.

5. The recommended values for the decoupling capacitors C3 and C4 are shown in the table below. These low ESR capacitors should be mounted as close to the driver circuit as possible. If needed, 1 μ F to 5 μ F ceramic capacitors may be added in parallel with C3 and C4.
6. If used, input filtering components (C1, C2 and LDM) should be mounted as close to the **MSC205-20** as possible. The PC board trace (or wire) between the DC/DC and the driver circuit should be as short as possible.
7. The use of tantalum capacitors in this circuit should be avoided.
8. The recommended values for components are:

Component	Value
TVS	SMBJ9.0
C1, C2	4.7 μ F/50V
LDM	6.8 μ H
C3, C4	100 μ F/35V

Mechanical Dimensions

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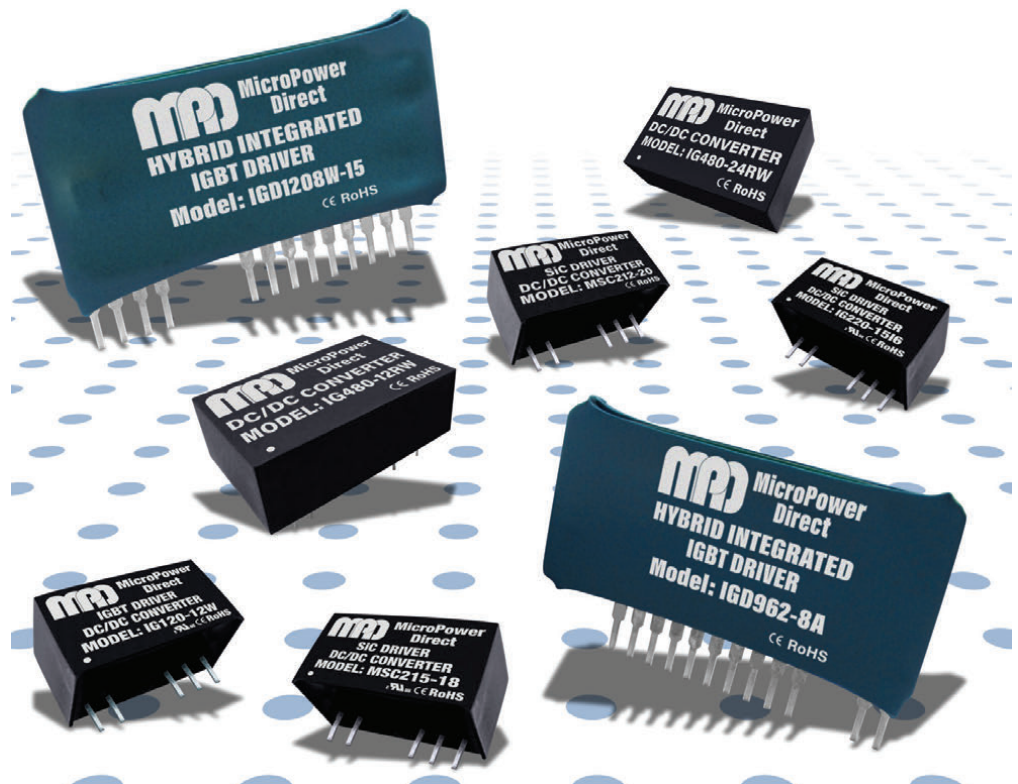
Pin Connections

Pin	Function
1	+VIN
2	Gnd
5	-VOUT
6	Common
7	+VOUT

Notes:

- All dimensions are typical in inches (mm)
- Pin 1 is marked by a "dot" or indentation on the unit
- General Tolerance = ± 0.01 (± 0.25)
- Pin Tolerance = ± 0.004 (± 0.10)
- Recommended pin hole size (on the application PC Board) is $\varnothing 0.039$ ($\varnothing 1.00$)
- Weight (Typ) = 0.148 Oz (4.2g)

MPD offers a very wide range of products specifically designed for use in high power, high speed gate drive circuits. Products include miniature DC/DC converters with asymmetrical outputs that fit the specific requirements of IGBT and SiC semiconductors. Also available are IGBT driver circuits that include much of the control circuit in a small SIP package. For full information, go to our website or contact the factory.



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